

Fig. 1

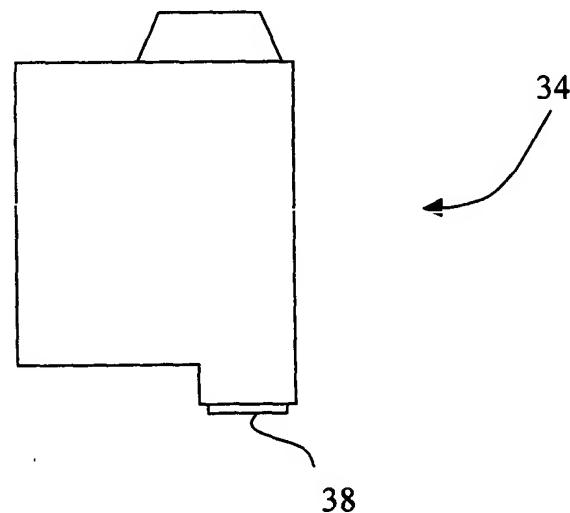


Fig. 2

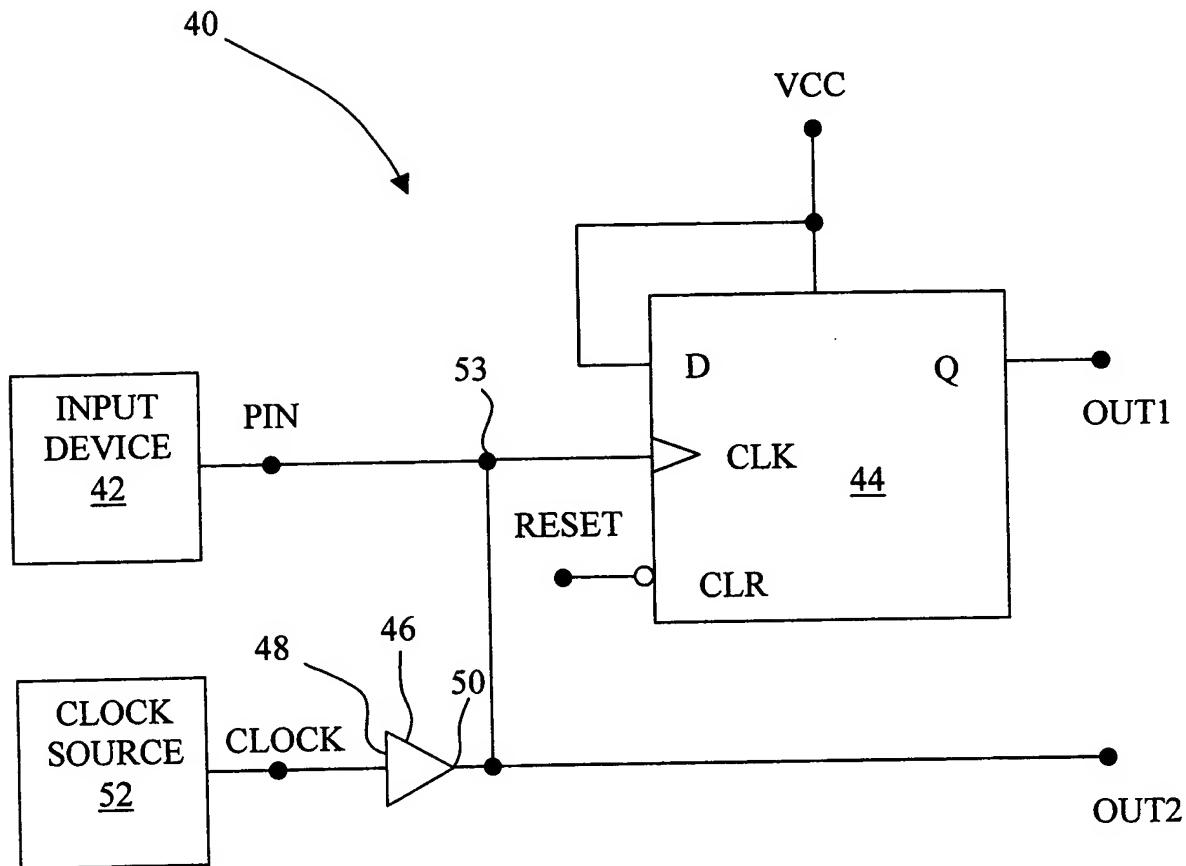


Fig. 3

OUT1	OUT2	PIN
1	X	FLOATING
0	1	1
0	0	0

Fig. 4

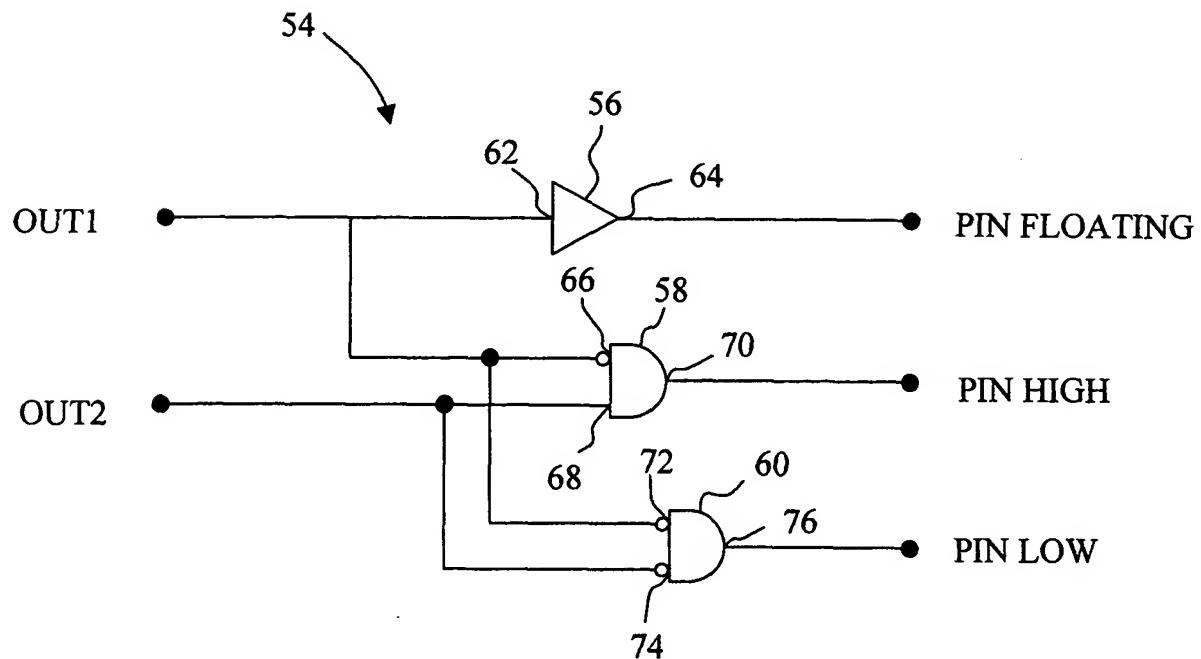


Fig. 5

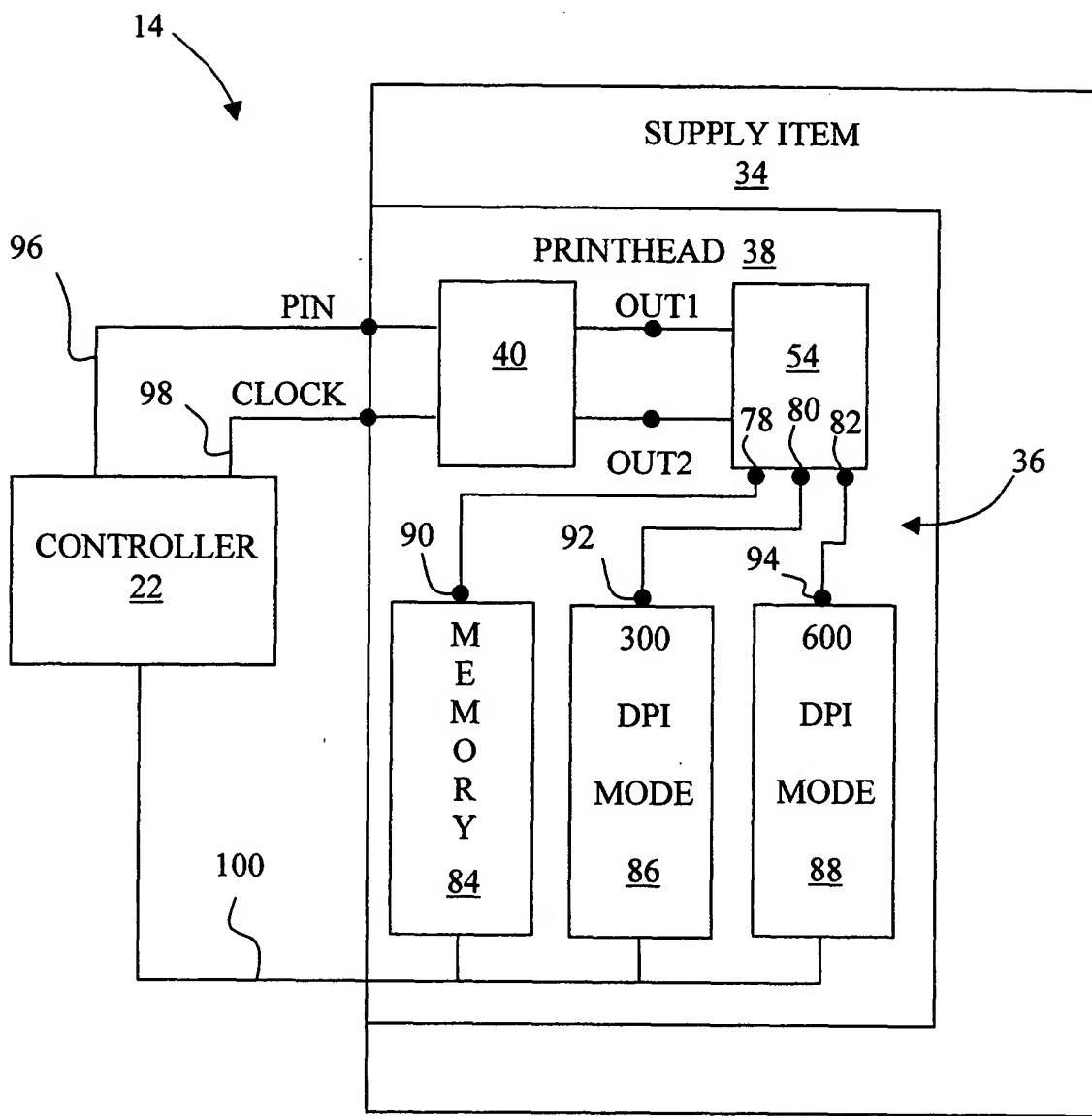


Fig. 6